



U.S. UTILITY Patent Application

PATENT NUMBER

OLPE

PATENT DATE

APPLICATION NO.

CONT/PRIOR

CLASS	
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SUBCLASS

ART UNIT

EXAMINER

09/928497

DEF

257

996

~~SECRET~~

164

APPLICANTS

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Semiconductor integrated circuit device and method of manufacturing the same

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawg.	Figs. Drawg.	Print Fig	Total Claims	Print Claim for D.G.
<input type="checkbox"/> The term of this patent as claimed to _____ (Date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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